

Appl. No. 10/065,482  
Amdt. dated January 09, 2006  
Reply to Office action of October 20, 2005

**Amendments to the Specification:**

Please replace paragraph [0005] with the following amended paragraph:

Please refer to Fig.1. Fig.1 is a block diagram of an optical disk system 10 and its  
5 periphery units according to the prior art. In Fig.1, an optical disk system control chip 200  
is used to update firmware information, which is stored in a memory 210, such as a flash  
memory 210 or an electrical erasable programmable ROM (EEPROM). The system  
control chip 200 includes an extra memory 202, such as a dynamic random access  
memory (DRAM), a microprocessor 204, a decoder 206, and a controller 208. The  
10 microprocessor 204 is separately coupled to the extra memory 202, the decoder 206, the  
controller 208, and the flash memory 210 so as to directly control the controller 208 and  
the decoder 206, and directly access the flash memory 210 and the extra memory 202.  
The decoder 206 and the controller 208 are also coupled together. The controller 208 is  
used to receive external control signals and information, such as control signals from a  
15 radio-frequency (RF) amplifier and controller 110 and information stored in an optical  
disk such as a digital versatile disk (DVD) or a compact disk (CD) 100 through the RF  
amplifier and controller 110. The decoder 206 is coupled to a buffer memory 212 external  
to the system control chip 200. The buffer memory 212 may be a DRAM and can  
communicate with a computer 216 through the controller 208 and a main board interface  
20 214. The main board interface 214 can be an IDE interface, an EIDE interface, a SCSI  
interface, an RS232 interface, a USB interface, or an IEEE 1394 interface.

Please replace paragraph [0032] with the following amended paragraph:

25 Please refer to Fig.4. Fig.4 is a block diagram of an optical disk system 20 according to  
the present invention. The optical disk system 20 shown in Fig.4 is identical to the optical  
disk system 10 shown in Fig.1 except the optical disk system control chip 200 has been  
replaced with an optical disk system control chip 201 having additional ~~for the addition of~~

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control circuitry 500. The control circuitry 500 is connected to the microprocessor 204 to help control operation of the microprocessor 204, as will be thoroughly explained below. Since all other components are the same, the reference numbers used in Fig.4 and in the following description will be the same numbers used in Fig.1.

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